

Evaluating Parallel Double-precision General Matrix Multiplication based on Blocked GEMM Algorithm on Intel Skylake clusters

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In high-performance computing, General Matrix-Matrix Multiplication routine (xGEMM) is crucial factor when systems deal with multiplications of matrices. Because modern applications use massive datasets which are equivalently represented in a matrix form. However, as increasing matrix sizes that are loaded on computing systems, the complexities of time consuming and calculation density relatively raises costs. It can be arranged by using programs in parallel computing environments, which processing units are physically and logically linked together in clusters [1]. Recent demands for domains of processing massive datasets indicate that computing workloads can be efficiently done by applying the high-performance computing techniques [2]. Thus, the core concepts mainly aim to how matrices can be handled for the specific computer architecture, and how broadcasting methods are applied during the multiplications.

As one of choices for matrix-matrix multiplication methods that we can try, the research of Blocked-GEMM algorithm for the Intel Knights Landing processor (KNL) has been previously conducted for processing double-precision GEMM (DGEMM) [3]. The research shows that the implementation uses the block matrices which fit in multi-levels of cache on the given architecture. And it helps sub-matrices to remain on the area of desired cache and register for less cache-miss occurrences on the calculations. The current research aims this algorithm to fit into Intel Skylake (SKL) clusters. It is shown that the blocked GEMM algorithm in SKL for scalable environments performs in the middle of between a naïve Intel Math Kernel Library (MKL) approach and a combination of ScaLAPACK with MKL attached. Future works yet to be done include that the adjustments should be improved for SKL clusters considering two major factors of achieving calculation performances and communication costs on each node.

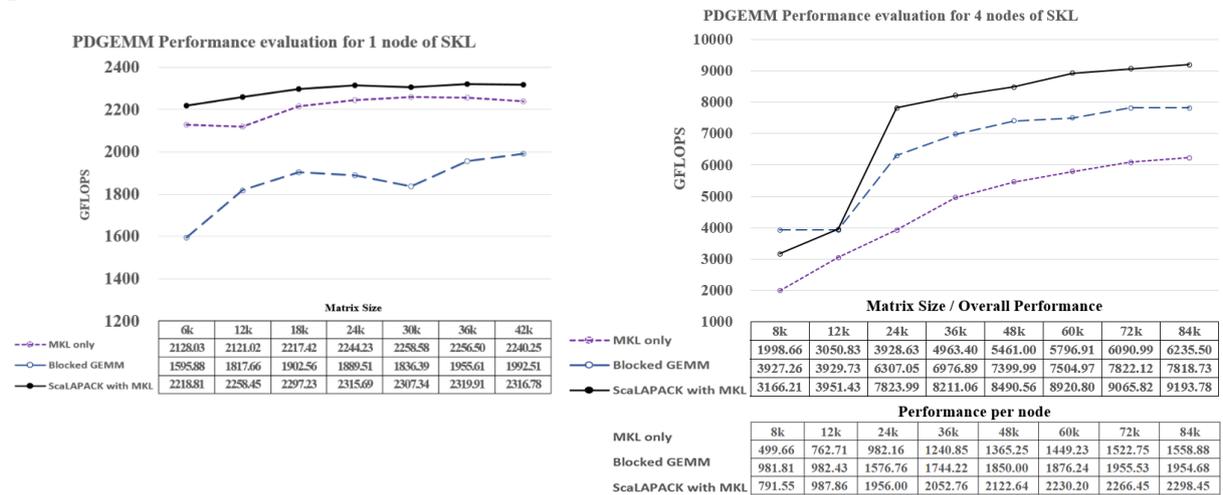


Figure 1. PDGEMM performance evaluations on SKL clusters: 1 node (left) and 4 nodes (right)

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References

- [1] D. Oliveira *et al.*, "Experimental and analytical study of Xeon Phi reliability," *Proceedings of the International Conference for High-Performance Computing, Networking, Storage and Analysis (SC '17)*, No. 28, pp. 1-12. (2017).
- [2] H. Anzt *et al.*, "Bringing High Performance computing to big data Algorithms," In *Handbook of Big Data Technologies*, Springer, pp. 777-806. (2017).
- [3] R. Lim *et al.*, "An implementation of matrix-matrix multiplication on the Intel KNL processor with AVX-512," In *Cluster Computing*, Vol. 21, No. 4, pp.1785-1795. (2018).